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Application Number	Not Yet Known	PTO
Filing Date	November 28, 2000	
First Named Inventor	Stephen M. Trimberger	S
Group Art Unit	Not Yet Known	Z132
Examiner Name	Not Yet Known	LEHNA
Attorney Docket Number	X-805-8 US	C784 U.S. 214734 09/11/2001

11/28/00

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

**Examiner
Signature**

Danson Fenn

Date Considered

10/08/2004

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known

Application Number	Not Yet Known
Filing Date	November 28, 2000
First Named Inventor	Stephen M. Trimberger
Group Art Unit	Not Yet Known 2132
Examiner Name	Not Yet Known LEMMA
Attorney Docket Number	X-805-8 US

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
S.L		Xilinx Application Note, "Configuration Issues: Power-Up, Volatility, Security, Battery Back-Up", XAPP 092, November 24, 1997 (Version 1.1).	
S.L		Virtex Configuration Guide, published by Xilinx, Inc., 2100 Logic Drive, San Jose, CA 95124, October 9, 2000 (Version 1.0).	
S.L		Bruce Schneier "Applied Cryptography: Protocols, Algorithms, and Source Code in C", Second Edition, Copyright 1996, published by John Wiley & Sons, Inc.; Chapter 9, pages 193-194 and 200-203; Chapter 12, pages 265-301 and Chapter 15, pages 360-361	
S.L		Cahners EDN ACCESS Web Page, "Cunning circuits confound crooks," October 12, 2000; pages 1-8; http://www.ednmag.com/ednmag/reg/2000/10122000/21df2.htm	
S.L		Xilinx, Application Note, XAPP138, "Virtex FPGA Series Configuration and Readback", published October 4, 2000, available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124.	

Examiner Signature

Date Considered

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